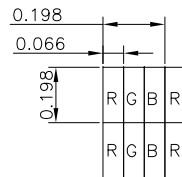
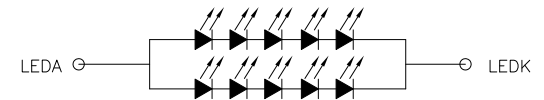


PIN ASSIGNMENT	
1	VDD
2	GND
3	SPI_SCLK/I2C_SCL
4	MISO/I2C_SDA
5	MOSI/I2C_SAO
6	CS/I2C_SA1
7	INT
8	PD
9	MODE
10	AUDIO_OUT
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	BLVDD
18	BLVDD
19	BLGND
20	BLGND



DETAIL A
(SCALE 35:1)



INTERNAL BACKLIGHT CTRCUIT DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT/TRANSMISSIVE/NORMALLY WHITE.
2. VIEWING DIRECTION: 12 O'CLOCK.
3. OPERATING VOLTAGE: VDD=3.3V(TYP).
4. CONTROLLER: FT800.
5. INTERFACE: 3-WIRE SPI/I2C.
6. OPERATING TEMP.: -20°C ~ 70°C.
7. STORAGE TEMP.: -30°C ~ 80°C.
8. BACKLIGHT: 10-CHIP WHITE LED,BUILT-IN BACKLIGHT DRIVER
9. SURFACE LUMINANCE: 440CD/M²(TYP.).
10. SURFACE TREATMENT: ANTI-GLARE
11. GENERAL TOLERANCE: ±0.3mm.
12. ROHS COMPLIANT.

MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	JACK	2018.05.09	SCALE : 1/1
CHECKED BY:			UNIT: mm
APPROVED BY:			DESCRIPTION:
VERSION NO:	01		TFT & RTP MODULE

VER.	FIRST ISSUE	JACK	2018.05.09
VER.	REVISED DESCRIPTION	REVISER	DATE